	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
Tuesday 01/10/2024						Practical, 01:30PM-03:30PM, Wk 9 Module: ENG6C2 (Digital Signal Processing) Staff: Ziegenhagen, Dominik Room: E1 CAD							

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	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
Tuesday 08/10/2024						Practical, 01:30PM-03:30PM, Wk 10 Module: ENG6C2 (Digital Signal Processing) Staff: Ziegenhagen, Dominik Room: E1 CAD							

	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
Tuesday 15/10/2024						Practical, 01:30PM-03:30PM, Wk 11 Module: ENG6C2 (Digital Signal Processing) Staff: Ziegenhagen, Dominik Room: E1 CAD							

	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
Tuesday 22/10/2024						Practical, 01:30PM-03:30PM, Wk 12 — Module: ENG6C2 (Digital Signal Processing) Staff: Ziegenhagen, Dominik Room: E1 CAD							

	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
Tuesday 29/10/2024						Practical, 01:30PM-03:30PM, Wk 13 Module: ENG6C2 (Digital Signal Processing) Staff: Ziegenhagen, Dominik Room: E1 CAD							

Module timetable - Digital Signal Processing (Wk 14, wk starting 04/11/2024)

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	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
Tuesday 05/11/2024						Practical, 01:30PM-03:30PM, Wk 14 Module: ENG6C2 (Digital Signal Processing) Staff: Ziegenhagen, Dominik Room: E1 CAD							

Module timetable - Digital Signal Processing (Wk 15, wk starting 11/11/2024)

CELCAT web publisher

	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
Tuesday 12/11/2024						Practical, 01:30PM-03:30PM, Wk 15 Module: ENG6C2 (Digital Signal Processing) Staff: Ziegenhagen, Dominik Room: E1 CAD							

Module timetable - Digital Signal Processing (Wk 16, wk starting 18/11/2024)

	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
Tuesday 19/11/2024						Practical, 01:30PM-03:30PM, Wk 16 Module: ENG6C2 (Digital Signal Processing) Staff: Ziegenhagen, Dominik Room: E1 CAD							

	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
Tuesday 26/11/2024						Practical, 01:30PM-03:30PM, Wk 17 Module: ENG6C2 (Digital Signal Processing) Staff: Ziegenhagen, Dominik Room: E1 CAD							

	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
Tuesday 03/12/2024						Practical, 01:30PM-03:30PM, Wk 18 Module: ENG6C2 (Digital Signal Processing) Staff: Ziegenhagen, Dominik Room: E1 CAD							

Module timetable - Digital Signal Processing (Wk 19, wk starting 09/12/2024)

	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
Tuesday 10/12/2024						Practical, 01:30PM-03:30PM, Wk 19 Module: ENG6C2 (Digital Signal Processing) Staff: Ziegenhagen, Dominik Room: E1 CAD							

Module timetable - Digital Signal Processing (Wk 21, wk starting 23/12/2024)

CELCAT
web publisher

		08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
We	25/12/2024	University closed Unavailable, Wk 21												
Thu	26/12/2024	University closed Unavailable, Wk 21												
Fri	27/12/2024	University closed Unavailable, Wk 21												
Sat	28/12/2024	University closed Unavailable, Wk 21												
Sun	29/12/2024	University closed Unavailable, Wk 21												

Module timetable - Digital Signal Processing (Wk 22, wk starting 30/12/2024)

		08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
Mo	30/12/2024	University closed Unavailable, Wk 22												
Tue	31/12/2024	University closed Unavailable, Wk 22												
We	01/01/2025	University closed Unavailable, Wk 22												

Module timetable - Digital Signal Processing (Wk 23, wk starting 06/01/2025)

	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
Tuesday 07/01/2025						Practical, 01:30PM-03:30PM, Wk 23 Module: ENG6C2 (Digital Signal Processing) Staff: Ziegenhagen, Dominik Room: E1 CAD							

	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
Tuesday 14/01/2025						Practical, 01:30PM-03:30PM, Wk 24 Module: ENG6C2 (Digital Signal Processing) Staff: Ziegenhagen, Dominik Room: E1 CAD							

Module timetable - Digital Signal Processing (Wk 37, wk starting 14/04/2025)

		08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
Fri	18/04/2025	Bank Holiday Unavailable, Wk 37												

Module timetable - Digital Signal Processing (Wk 38, wk starting 21/04/2025)

		08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
Mo	21/04/2025	Bank Holiday Unavailable, Wk 38												
Tue	22/04/2025	University closed Unavailable, Wk 38												

Module timetable - Digital Signal Processing (Wk 40, wk starting 05/05/2025)

		08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
Mo	05/05/2025	Bank Holiday Unavailable, Wk 40												

Module timetable - Digital Signal Processing (Wk 43, wk starting 26/05/2025)

		08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
Mo	26/05/2025	Bank Holiday Unavailable, Wk 43												

		08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
Mo	25/08/2025	Bank Holiday Unavailable, Wk 4												