

Module timetable - Digital Signal Processing (Wk 9, wk starting 30/09/2024)

	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
day 2024						30PI Modu	tical, 01:30PN M, Wk 9 ule: ENG6C2						
Tues 01/10/2						Proc Staff Dom	tal Signal essing) : Ziegenhage inik n: <u>E1 CAD</u>	n,					



Module timetable - Digital Signal Processing (Wk 10, wk starting 07/10/2024)

	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
Tuesday 08/10/2024						30PM Modu (Digit Proc Staff: Dom	tical, 01:30PM M, Wk 10 ule: ENG6C2 tal Signal essing) : Ziegenhage inik m: E1 CAD						



Module timetable - Digital Signal Processing (Wk 11, wk starting 14/10/2024)

	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
Tuesday 15/10/2024						30PM Modu (Digit Proc Staff Dom	tical, 01:30PM, Wk 11 ule: ENG6C2 tal Signal essing) : Ziegenhage inik m: E1 CAD						



Module timetable - Digital Signal Processing (Wk 12, wk starting 21/10/2024)

	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
Tuesday 22/10/2024						30PM Modu (Digit Proc Staff Dom	tical, 01:30PN M, Wk 12 ule: ENG6C2 tal Signal essing) : Ziegenhage inik m: E1 CAD						



Module timetable - Digital Signal Processing (Wk 13, wk starting 28/10/2024)

	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
Tuesday 29/10/2024						30PI Modu (Digit Proc Staff Dom	tical, 01:30PM, Wk 13 ule: ENG6C2 tal Signal essing) : Ziegenhage inik m: E1 CAD						



Module timetable - Digital Signal Processing (Wk 14, wk starting 04/11/2024)

	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
Tuesday 05/11/2024						30PM Modu (Digit Proc Staff Dom	tical, 01:30PM, Wk 14 ule: ENG6C2 tal Signal essing) : Ziegenhage inik m: E1 CAD						



Module timetable - Digital Signal Processing (Wk 15, wk starting 11/11/2024)

	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
esday 1/2024						30PM Modu (Digit	tical, 01:30PM, Wk 15 ule: ENG6C2 tal Signal essing)						
Tu 12/1						Staff Dom	: Ziegenhage	n,					



Module timetable - Digital Signal Processing (Wk 16, wk starting 18/11/2024)

	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
Tuesday 19/11/2024						30PI Modu (Digit Proc Staff Dom	tical, 01:30PM, Wk 16 ule: ENG6C2 tal Signal essing) : Ziegenhage inik m: E1 CAD						



Module timetable - Digital Signal Processing (Wk 17, wk starting 25/11/2024)

	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
esday 11/2024						30PI Modu (Digit Proc	tical, 01:30PM, Wk 17 ule: ENG6C2 tal Signal essing)						
Tu 26/7						Dom	: Ziegenhage iinik m: <u>E1 CAD</u>	n,					



Module timetable - Digital Signal Processing (Wk 18, wk starting 02/12/2024)

	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
Tuesday 03/12/2024						30PI Modu (Digit Proc Staff Dom	tical, 01:30PM, Wk 18 ule: ENG6C2 tal Signal essing) : Ziegenhage inik m: E1 CAD						



Module timetable - Digital Signal Processing (Wk 19, wk starting 09/12/2024)

	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
Tuesday 10/12/2024						30PM Modu (Digit Proc Staff Dom	tical, 01:30PM, Wk 19 Lile: ENG6C2 tal Signal essing) : Ziegenhage inik m: E1 CAD						



Module timetable - Digital Signal Processing (Wk 21, wk starting 23/12/2024)

		08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
We	25/12/2024	University c Unavailable	losed e, Wk 21	'	'		'	'	'		'	'	'	
>	25/1													
hu	26/12/2024	University c Unavailable	losed e, Wk 21											
—	26/1													
Fri	27/12/2024	University c Unavailable		'	'		'	'	'	'	'	'	'	
	27/1													
Sat	28/12/2024	University c Unavailable		'	'	'	'	'	'	'	'	'	'	
S	28/1													
un	29/12/2024	University c Unavailable	losed e, Wk 21	'	'		'	'	'		'	'		
S	29/1													



Module timetable - Digital Signal Processing (Wk 22, wk starting 30/12/2024)

		08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM	
Mo		University of Unavailable	losed e, Wk 22	'	'			'			1	1	'		
_	30/1														
ne	2/2024	University of Unavailable	Iniversity closed Inavailable, Wk 22												
\vdash	31/1														
We	1/2025	University of Unavailable	losed e, Wk 22											·	
>	01/01														



Module timetable - Digital Signal Processing (Wk 23, wk starting 06/01/2025)

	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
Tuesday 07/01/2025						30PI Modu (Digit Proc Staff Dom	tical, 01:30PM, Wk 23 ule: ENG6C2 tal Signal essing) : Ziegenhage inik m: E1 CAD						



Module timetable - Digital Signal Processing (Wk 24, wk starting 13/01/2025)

	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
Tuesday 14/01/2025						30PM Modu (Digit Proc Staff Dom	tical, 01:30PM, Wk 24 ule: ENG6C2 tal Signal essing) : Ziegenhage inik m: E1 CAD						



Module timetable - Digital Signal Processing (Wk 37, wk starting 14/04/2025)

i_	025	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
		Bank Holida Unavailable											•	
	18/0													



Module timetable - Digital Signal Processing (Wk 38, wk starting 21/04/2025)

		08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
10	4/2025	Bank Holida Unavailabl	ay e, Wk 38											
_	21/0													
ne	4/2025	University of Unavailable	closed e, Wk 38			'	'		'	,			'	
—	22/0													



Module timetable - Digital Signal Processing (Wk 40, wk starting 05/05/2025)

4o		08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
	5/2025	Bank Holida Unavailable												
_	02/0													



Module timetable - Digital Signal Processing (Wk 43, wk starting 26/05/2025)

0		08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
	5/2025	Bank Holida Unavailable												
2	26/0													



Module timetable - Digital Signal Processing (Wk 4, wk starting 25/08/2025)

4o		08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM
	8/2025	Bank Holida Unavailable												
_	25/0													